

























Fault Tolerance

- Coordinated checkpoint/restart
- Uncoordinated checkpoint/restart
 - Improved Message Logging (under 5% overhead).
- Support BLCR and self
- Able to handle real process migration (i.e. change the network during the migration)
 - MX, IB, TCP, SM, self







Major Features (so far)

- Linux KNEM support
- Broke ABI
- Revamped run-time support
- Some (but not all) MPI-2.2 support
- Scalability enhancements

- Dynamic process improvements
- Portability updates
 - BSD, Catamount, Windows, OS X, Solaris
- Millions of other little improvements, updates, and bug fixes

1.5.x Roadmap

- 1.5.1 to be released "soon"
 - Minor bug fixes against 1.5.0
 - No real new features
 - Too stressful to do a correct SC release
- Expected in December 2010



• 1.5.2

- ROMIO refresh
- Hwloc hardware affinity (back-end)
- Linux UMMU notify (possible)
- "Better" process affinity (more in later slides)
- 1.5.3
 - "It depends"







Reminder: OMPI Internals





Threading

- Playground for asynchronous progress
 - Design ready for TCP
 - Implementation underway
- MPI_THREAD_MULTIPLE
 - Minimize the overhead
 - More safety for "non-sexy" parts of MPI
 - Performance tuning











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2 (256KB)	L2 (256KB)					
1 (32KB)	L1 (32KB)					
lore p#2	Core p#3					
PU p#9	PU p#13					
PU p#41	PU p#45					
2 (256KB)	L2 (256KB)					
1 (32KB)	L1 (32KB)					
ore p#10	Core p#11					
PU p#25	PU p#29					
PU p#57	PU p#61					
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		L3 (24MB)				L3 (24MB)					
			L2 (512KB)		L2 (512KB)		L2 (512KB)	L2 (512KB)			
			L1 (32KB) L1 (32K	(8)	L1 (32KB) L1 (32KB)		L1 (32KB) L1 (32KB)	L1 (32KB) L1 (32KB)			
			4		Core p#2 PU p#8 PU p#40 PU p#44		6	Core p#2 PU p#9 PU p#41 Core p#3 PU p#13 PU p#45			
			L2 (512KB)		L2 (512KB)		L2 (512KB)	L2 (512KB)			
			L1 (32KB) L1 (32K	(B)	L1 (32KB) L1 (32KB)		L1 (32KB) L1 (32KB)	L1 (32KB) L1 (32KB)			
			5		Core p#10 PU p#24 PU p#56 Core p#11 PU p#28 PU p#60		7	Core p#10 PU p#25 PU p#57 Core p#11 PU p#29 PU p#61			



Flexibility

- Need to represent:
 - Hardware threads, cores, L2 / L3 caches, sockets, boards, nodes
- Need to handle heterogeneous situations
 - E.g., non-uniform socket core count
- Would be nice to handle "offline" units

How To Express?

- Incredibly challenging to represent this on a command line
 - Need to be simple for 95% of users
 - Need to be powerful / flexible for power users
- May introduce "rankfile2" syntax
 - More flexible than current "rankfile" syntax
 - Allow completely arbitrary binding and ordering
- Design discussions are ongoing





KNEM details

- Dedicated Linux kernel module
 - Working for any kernel since 2.6.15
- RMA interface
 - · Sender creates a memory region, gets a cookie
 - Cookie is passed to another process (using PML)
 - · Receiver pulls data from the send region
- Vectorial buffers, asynchronous data transfers, ...
- DMA engine support disabled by default
 - Bad on current platforms

KNEM Future

- Use KNEM directly inside collective
 - · Use same memory region multiple times, read or write
 - Instead of only using KNEM for point-to-point within collectives
- SSE optimization (not that easy in the kernel)
- Thinking about getting some official support in Linux
 - Christopher Yeoh (IBM) trying to push some basic support
 - Not region based, not vectorial, ...
 - Full vectorial and region-based support needs more discussion

Hardware Locality (hwloc)

Replaces PLPA

- Working towards including in OMPI 1.5.x
 - New paffinity component
- More knowledge of the topology
 - HMT/SMT, shared caches, NUMA, ...
- Portable
 - Solaris, AIX, OSF, HP-UX, FreeBSD, Darwin, Windows
 - Topology discovery and binding abilities may vary



Hwloc Future

I/O device discovery

- hwloc 1.1 already knows the affinity of I/O handles
 - Cuda devices, IB devices, MX endpoints, ...
- · Working on adding these objects to the main tree
- May be used for tuning Open MPI components
- NUMA distances









- MPI-3 has a "freely available implementation" requirement
 - Much work being prototyped in Open MPI
 - Will help speed our final implementation
- Examples
 - Fault tolerance work (Josh Hursey, ORNL)
 "Crossing the Valley of Death"
 - New Fortran MPI bindings (Craig Rasmussen, LANL)

